

AMENDMENT UNDER 37 C.F.R. § 1.111  
U.S. Application No. 10/089,676

**AMENDMENTS TO THE DRAWINGS**

FIGS. 2 and 3 are labeled "prior art".

**Attachment: 2 Replacement Sheets**

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REMARKS

Reconsideration and allowance of the subject application are respectfully requested.

Upon entry of this Amendment, claims 1-4 are pending in the application. Applicant respectfully submits that the pending claims define patentable subject matter.

The drawings are objected to because the Examiner maintains that Figures 2 and 3 should be labeled "prior art". Applicant is submitting replacement drawings wherein Figures 2 and 3 are labeled "prior art". Accordingly, the Examiner is requested to remove the objection to the drawings.

Claims 1-4 are rejected under 35 U.S.C. § 102(b) as being anticipated by Betts (U.S. Patent No. 4,430,745). Applicant respectfully traverses the rejection.

Amended independent claim 1 recites:

a first microcomputer having a communication function for outputting a data signal including data frames, each including a plurality of data bits and having a data time  $T_d$  and a transmission period  $T_p$ ; and

a second microcomputer for receiving said data signal, sequentially storing said data frames in a memory, and reading and processing said data frames by timing pulses, wherein said data time  $T_d$  and said transmission period  $T_p$  have a relation of  $T_d < T_p/2$ .

Betts discloses a data transmission system in which data is transmitted along a transmission path at a rate  $C$  greater than an upper data rate limit  $A$  of the path. A data transmitter driven by an internal clock at  $C$  Hertz transmits data along the transmission path and at the same time also transmits a derived clock signal at a frequency  $C/N$  Hertz where  $(C/N) < A$  along the path. At the receiver the data is clocked into a series of data stores (flip-flops) under

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the control of the transmitted clock signal and is thereafter recombined into a serial stream under the control of the clock signal driving the receiver.

Applicant respectfully submits that Betts does not teach or suggest that the data signal includes data frames including a plurality of bits and having a data time  $T_d$ . Each of the pulses A1-A3 of the data signal shown in Figure 3 of Betts are one bit such that the one bit of data is stored in and output from the flip-flops (e.g., flip-flops 8 and 9 in Figure 2) when the clock signal transitions (i.e., data bit A1 is stored in flip-flop 8 on the rising edge of the clock and data bit A2 is stored in the flip-flop 9 on the falling edge of the clock).

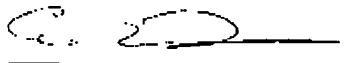
Accordingly, Applicant respectfully submits that claims 1-4 would not have been anticipated by or rendered obvious in view of Betts because the cited reference does not teach or suggest the claimed feature of transmitting data frames including a plurality of bits and having a data time  $T_d$  and a transmission period  $T_p$ , wherein  $T_d < T_p/2$ .

In view of the above, reconsideration and allowance of this application are now believed to be in order, and such actions are hereby solicited. If any points remain in issue which the Examiner feels may be best resolved through a personal or telephone interview, the Examiner is kindly requested to contact the undersigned at the telephone number listed below.

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The USPTO is directed and authorized to charge all required fees, except for the Issue Fee and the Publication Fee, to Deposit Account No. 19-4880. Please also credit any overpayments to said Deposit Account.

Respectfully submitted,

  
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CUSTOMER NUMBER

Date: November 17, 2005

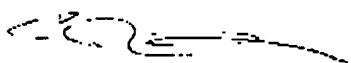
Attorney Docket No.: Q68877

**CERTIFICATION OF FACSIMILE TRANSMISSION**

Sir:

I hereby certify that the above identified correspondence is being facsimile transmitted to Examiner David B. LUGO at the Patent and Trademark Office on November 17, 2005 at 571-273-8300.

Respectfully submitted,

  
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